



501.39055CX1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: N. HASEGAWA, et al.  
Serial No: 10/674,381  
Filed: October 1, 2003  
Title: METHOD OF MANUFACTURING SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE, OPTICAL MASK  
THEREFOR, ITS MANUFACTURING METHOD, AND MASK  
BLANKS  
Group: 1756  
Examiner: Saleha R. MOHAMEDULLA

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

July 19, 2005

Sir:

In response to the Office Action mailed January 19, 2005, please  
amend the above-identified application as listed in the following, and as set  
forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.

07/20/2005 HRL111 00000071 10674381

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